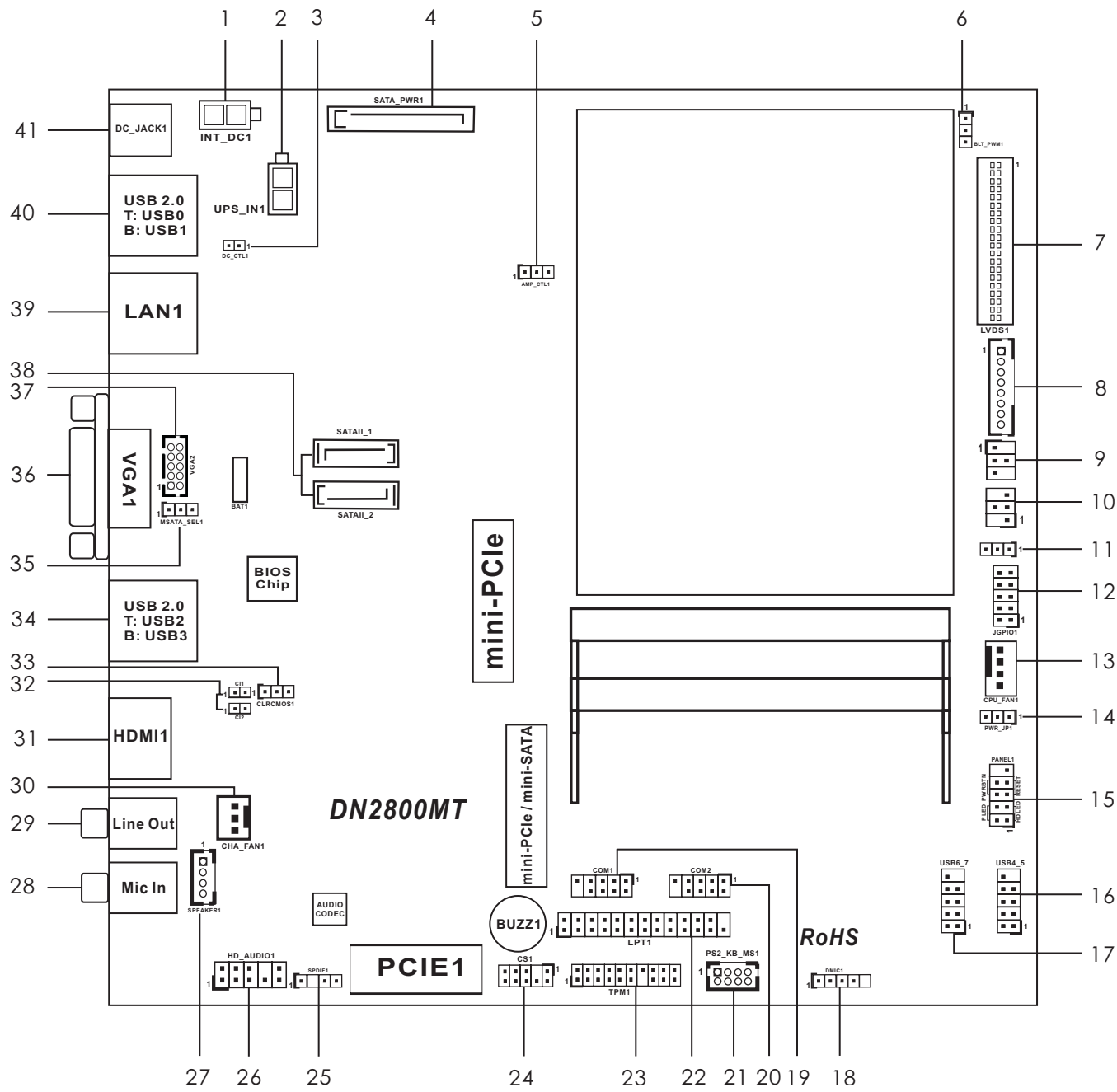
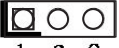


DN2800MT Jumpers and headers setting guide



- 1 : 2-pin ATX Power Input/Output Connector
- 2 : 2-pin UPS Module Power Input Connector
- 3 : DC_CTL1  1
Power Input Voltage > +12V: Short
Power Input Voltage ≤ +12V: Open

- 4 : SATA Power Output Connector
- 5 : AMP_CTL1  1 2 3
- | PIN | Signal Name |
|-----|-------------|
| 1 | GPIO_VOL_UP |
| 2 | GND |
| 3 | GPIO_VOL_DW |

- 6 : BLT_PWM1
1-2: +3V
2-3: +5V 

7 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	R_LVDD	1	R_LVDD
4	LDDC_CLK	3	+3V
6	LVDS_A_DATA0#	5	LDDC_DATA
8	GND	7	LVDS_A_DATA0
10	LVDS_A_DATA1#	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3#	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	GND	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKLT_EN_R	35	GND
38	LCD_BLT_VCC	37	CON_LBKLT_CTL_R
40	LCD_BLT_VCC	39	LCD_BLT_VCC

8 : BLT_CTL1

PIN	Signal Name
1	CON_LBKLT_EN
2	CON_LBKLT_CTL
3	LCD_BLT_VCC
4	LCD_BLT_VCC
5	GND
6	GND
7	GPIO_BLT_UP
8	GPIO_BLT_DW

9 : PNL_PWR1

PIN	Signal Name
1	+3.3V
2	NC
3	LCD_VCC
4	+12V
5	+5V
6	NC

10 : BKT_PWR1

PIN	Signal Name
1	+5V
2	NC
3	BKLT_PWR
4	+Vin
5	+12V
6	NC

- 11 : Digital Input / Output Power select
1-2: +12V
2-3: +5V 

12 : Digital Input / Output Pin Header

PIN	Signal Name	PIN	Signal Name
1	Digital Output 0	2	Digital Input 0
3	Digital Output 1	4	Digital Input 1
5	Digital Output 2	6	Digital Input 2
7	Digital Output 3	8	Digital Input 3
9	JGPIO_PWR1	10	GND

- 13 : 4-Pin CPU FAN Connector (+12V)
- | | Signal Name |
|---|-------------------|
| 1 | GND |
| 2 | +12V |
| 3 | CPU_FAN_SPEED |
| 4 | FAN_SPEED_CONTROL |

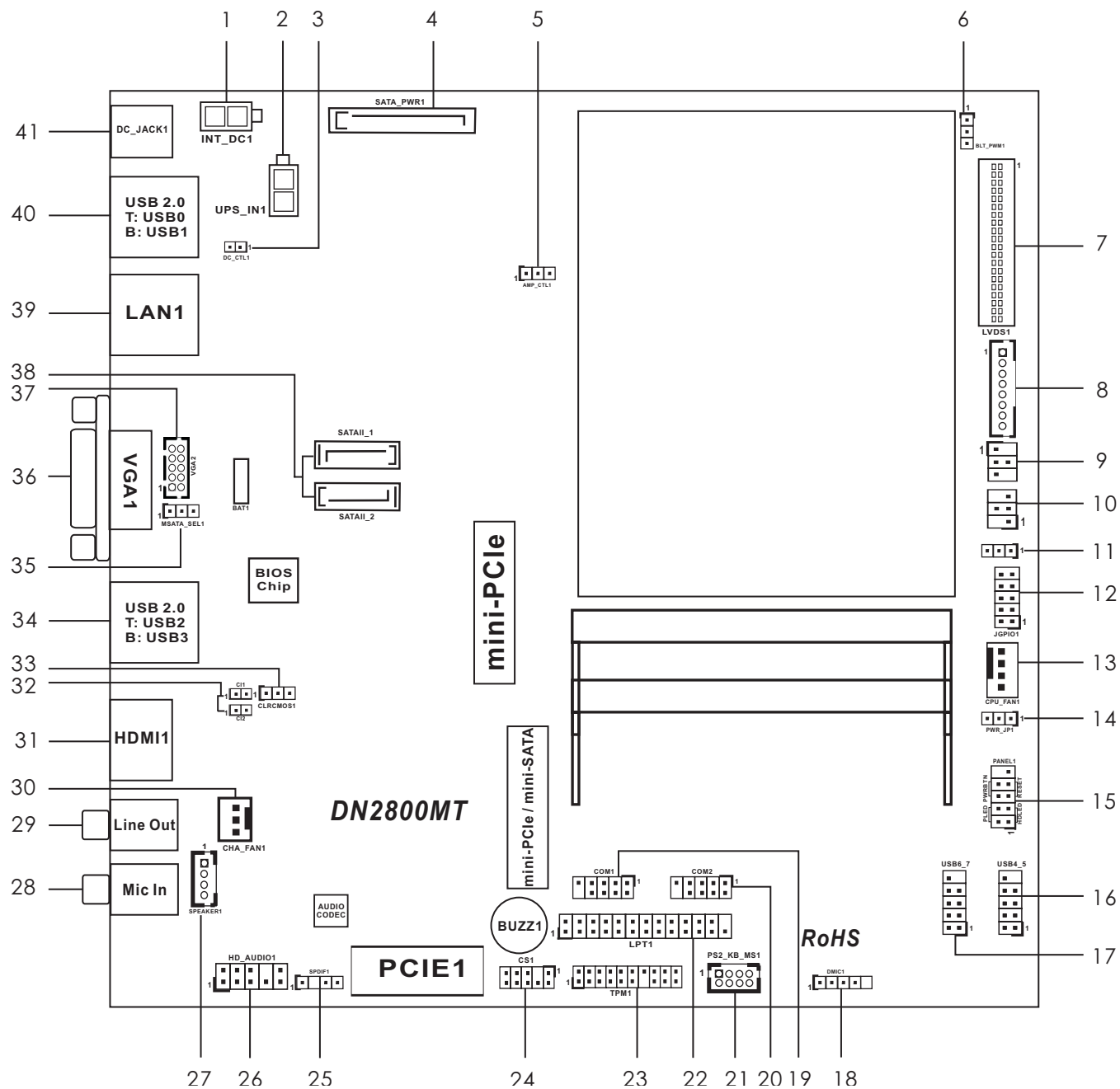
- 14 : ATX/AT mode Selection
1-2: AT mode
2-3: ATX mode 

- 15 : System Panel Header
- | | Signal Name |
|---|-------------|
| 1 | PLED+ |
| 2 | PLED- |
| 3 | PWRBTN# |
| 4 | GND |
| 5 | +5V |
| 6 | RESET# |
| 7 | GND |
| 8 | HDLED- |
| 9 | HDLED+ |

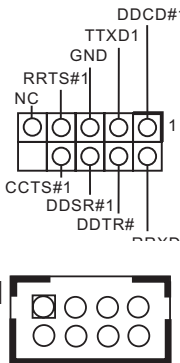
- USB2.0 Headers
16 : USB4_5
17 : USB6_7
- | | Signal Name |
|---|-------------|
| 1 | USB_PWR_B |
| 2 | +B |
| 3 | GND |
| 4 | HOLED- |
| 5 | GND |
| 6 | +A |
| 7 | -A |
| 8 | USB_PWR |

18 : DMIC1

PIN	Signal Name
1	+3V
2	DMIC_DATA
3	GND
4	DMIC_CLK
5	NC



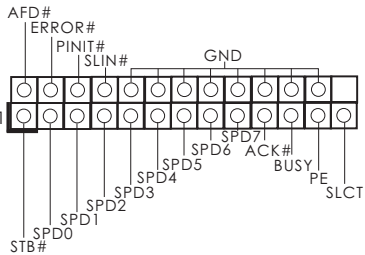
RS-232 Port 4 Pin Headers
19 : COM1
20 : COM2



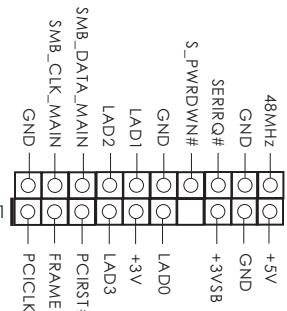
21 : PS2_KB_MS1

PIN	Signal Name
1	KBCLK
2	+5V
3	KBDATA
4	+5V
5	MSDATA
6	GND
7	MSCLK
8	GND

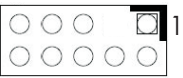
22 : Printer Port Header



23 : TPM Header

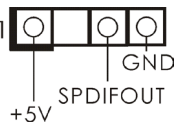


24 : CS1

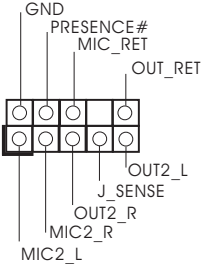


PIN	Signal Name
1	Watch Dog Timer
2	Ground
3	NC
4	SMB_CLK_RESUME
5	+3.3V standby
6	SMB_DATA_RESUME
7	PWRBT#
8	CIRR#
9	+5.0V standby
10	Ground

25 : SPDIF1



26 : Front Panel Audio Header



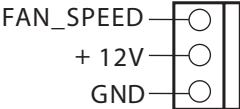
27 : 3W Audio AMP Output Wafer

PIN	Signal Name
1	SPK L-
2	SPK L+
3	SPK R+
4	SPK R-



28 : Audio Jack
Pink - Mic In
29 : Audio Jack
Green - Line Out

30 : 3-Pin Chassis FAN Connector (+12V)

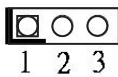


31 : HDMI1

32 : Chassis Intrusion Headers (CI1, CI2)

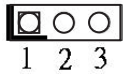


33 : Clear CMOS Header
1-2: Normal
2-3: Clear CMOS



34 : USB2.0 (USB2_3)

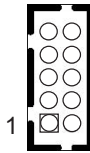
35 : MSATA_SEL1 (Disable SATAII_2)
1-2: mPCIE
2-3: mSATA (Disable SATAII_2)



36 : VGA1

37 : VGA2

PIN	Signal Name	PIN	Signal Name
1	RED	2	GND
3	GRN	4	GND
5	BLUE	6	GND
7	HSYNC	8	VSYSN
9	DDC_CLK	10	DDC_DATA



38 : SATA2 Connectors (SATAII_1, SATAII_2)



39 : RJ45 LAN Port 1

40 : USB2.0 (USB0_1)

41 : DC Jack1